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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of Tomoyuki Furuhashi)
Serial No.: 10/014,584 ✓)
Filing Date: December 14, 2001 ✓)
For: SEMICONDUCTOR DEVICES HAVING)
A NON-VOLATILE MEMORY)
TRANSISTOR AND METHODS FOR)
MANUFACTURING THE SAME)

Group Art Unit: 2826

Examiner: Mandala, Victor A.

AMENDMENT

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sirs:

In response to the Office Action dated Feb. 4, 2003, the response being due by Monday, May 5, 2003 (May 4 was a Sunday), please enter and consider the following.

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IN THE CLAIMS:

Please amend claims 11 and 18 as follows:

B1
11. (amended) A semiconductor device having a non-volatile memory transistor, comprising a non-volatile memory transistor including a semiconductor layer, a floating gate disposed above the semiconductor layer, and a control gate formed above the floating gate, wherein a conduction layer is provided above the non-volatile memory transistor and at least a portion of the conduction layer is located vertically above the floating gate along the entire length of the floating gate, and a width of the conduction layer located vertically above the floating gate is formed to be greater than a width of the floating gate.

B2
18. (amended) A semiconductor device having a non-volatile memory transistor according to claim 1, wherein the non-volatile memory transistor comprises a first circuit region, and wherein the semiconductor device further comprises a second circuit region, wherein the first circuit region and the second circuit region are formed in a sea of gates structure.

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Commissioner for Patents
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Sir:

Transmitted herewith is an Amendment in the above-identified application.

☐ Small entity status of this application under 37 CFR 1.9 and 1.27 has been established by a verified statement previously submitted.

☐ A verified statement to establish small entity status under 37 CFR 1.9 and 1.27 is enclosed.

☒ It is believed that no additional fee is required.

The fee has been calculated as shown below:

| | CLAIMS REMAINING AFTER AMENDMENT | | HIGHEST NO PREVIOUSLY PAID FOR | | PRESENT EXTRA RATE | | ADDIT. FEE | OR | RATE | | ADDIT. FEE |
|--|---|-------|--------------------------------------|---|-----------------------|---|---------------|----|-------|--|---------------|
| TOTAL | 27 | MINUS | 27 | = | 0 | x | \$9 | OR | x 18 | | \$0 |
| INDEP CLAIMS * | 9 | MINUS | 9 | = | 0 | x | \$42 | OR | x 84 | | \$0 |
| <input type="checkbox"/> FIRST PRESENTATION OF MULTIPLE DEP. CLAIM | | | | | | + | \$140 | OR | + 280 | | \$0 |
| | | | | | TOTAL | | \$0 | OR | TOTAL | | \$0 |

☐ Please charge Deposit Account No. 50-0585 the amount of \$___ to cover the extension fee and also the amount of \$___ to cover the claim fee. A duplicate copy of this sheet is enclosed.

☐ A check in the amount of \$___ to cover the extension fee is enclosed.

☐ A check in the amount of \$___ to cover the filing fee for additional claims is enclosed.

☐ A check in the amount of \$___ to cover the RCE fee is enclosed.

☐ A check in the amount of \$___ to cover the Information Disclosure Statement fee is enclosed.

☒ The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-0585. A duplicate of this sheet is enclosed.

☒ Any filing fees under 37 CFR 1.16 for the presentation of extra claims.

☒ Any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

Alan S. Raynes
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Dated: May 5, 2003

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Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop Non-Fee Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on May 5, 2003.

Alan S. Raynes
Alan S. Raynes

May 5, 2003
Date